IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: H. MATSUOKA et al

Serial No. 09/592,648 Filed: June 13, 2000

Group Art:

2814

Examiner:

D. Nguyen

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

AND A METHOD OF MANUFACTURE THEREOF

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

July 31, 2003

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 09/592,648.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

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Date: July 31, 2003

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.	SERIA	SERIAL NO.			
					APPLICANT				
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)					H. MATSUOKA et al FILING DATE GROUP				
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U.S. PATENT DOCUMENTS									
* EXAMINER INITIAL		DOCUMENT	DATE		NAME	CLASS	SUBCLASS	FILING (If Appr	
	AA	6,130,449	10/2000	Matsuoka et al					
	АВ	6,100,117	08/2000	Hao et al					
	AC								
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FOREIGN PATENT DOCUMENTS CLASS SUPPLIANCE TRANSLATIO									LATIO
		DOCUMENT	DATE		COUNTRY	CLASS	SUBCLASS	YES	NO
	AL	07-066299	03/10/95	Japan					
	АМ								
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)									
	AR	Ahn et al "Bidirectional Matched Global Bit Line Scheme for High Density							
		DRAMs", Symposium on VLSI Circuits, 1993, pp. 91-92.							
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EXAMINER					DATE CONSIDERED				
* EXAMINER	R: Initia	a) if reference considere	ed, whether or not citatio	n is in conformance with MPE	P 609; Draw line through citation if not in	conformance and no	t considered. Include	copy of thi	s form